

FIG.1

FIG. 2 is a block diagram of a storage system 100 in accordance with an embodiment of the present invention. The storage system 100 includes a host computer 200, a buffer memory 210, an interface 202, a read/write channel (RW) channel 212, an ECC engine 213, a microprocessor 216, a spindle control 226, a servo control 228, a disk 108, and a flash/ROM 224. The host computer 200 is connected to the buffer memory 210 via control lines 208 and data lines 206. The buffer memory 210 is connected to the interface 202 via data lines 202. The interface 202 is connected to the RW channel 212 and the ECC engine 213 via control lines 204 and data lines 222. The RW channel 212 and the ECC engine 213 are connected to the microprocessor 216 via control lines 218 and data lines 220. The microprocessor 216 is connected to the spindle control 226 and the servo control 228 via control lines 216. The spindle control 226 is connected to the disk 108 via control lines 226. The servo control 228 is connected to the disk 108 via control lines 228. The disk 108 is connected to the microprocessor 216 via control lines 216. The microprocessor 216 is connected to the flash/ROM 224 via control lines 224 and data lines 224.

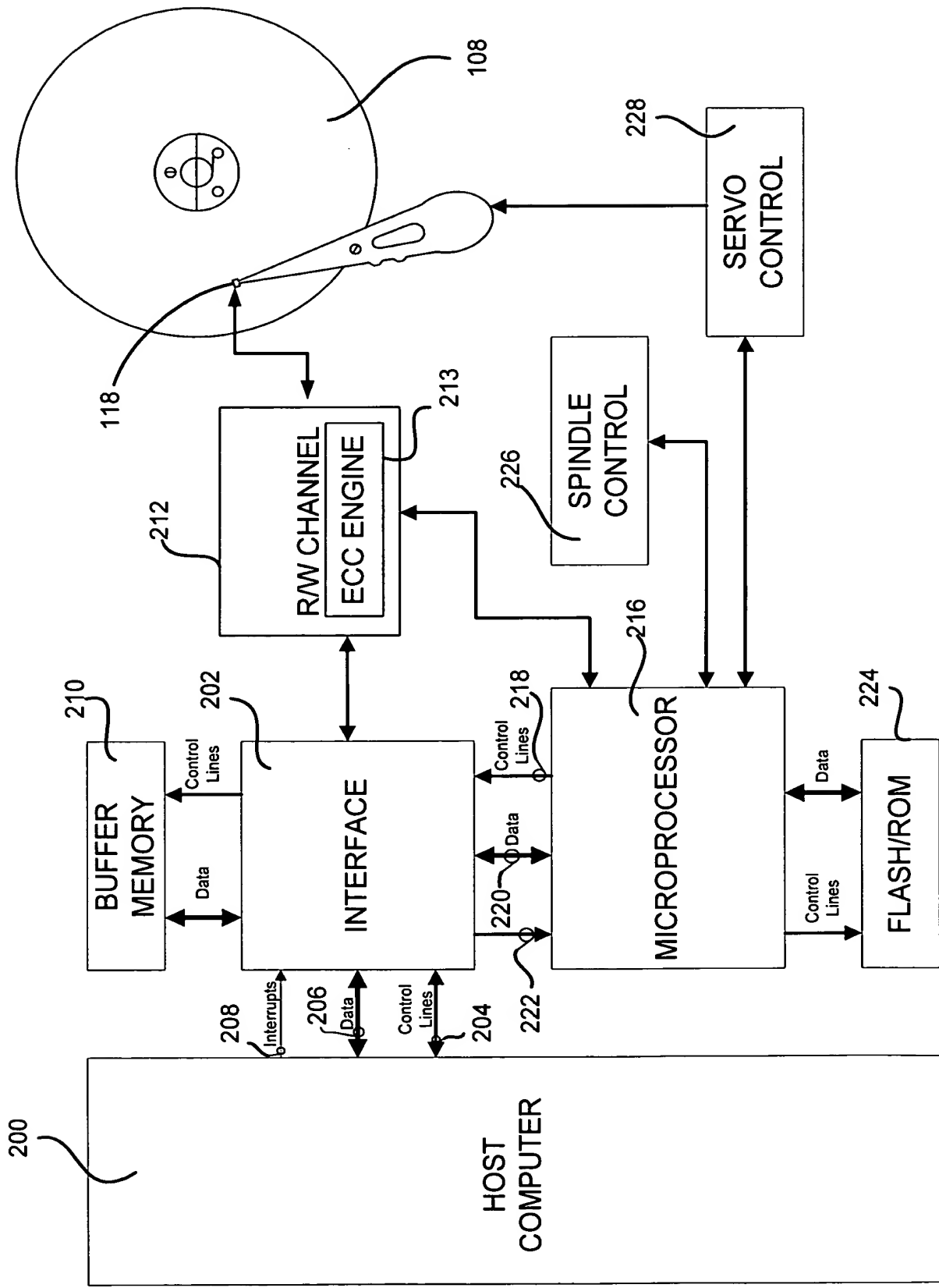


Fig. 2

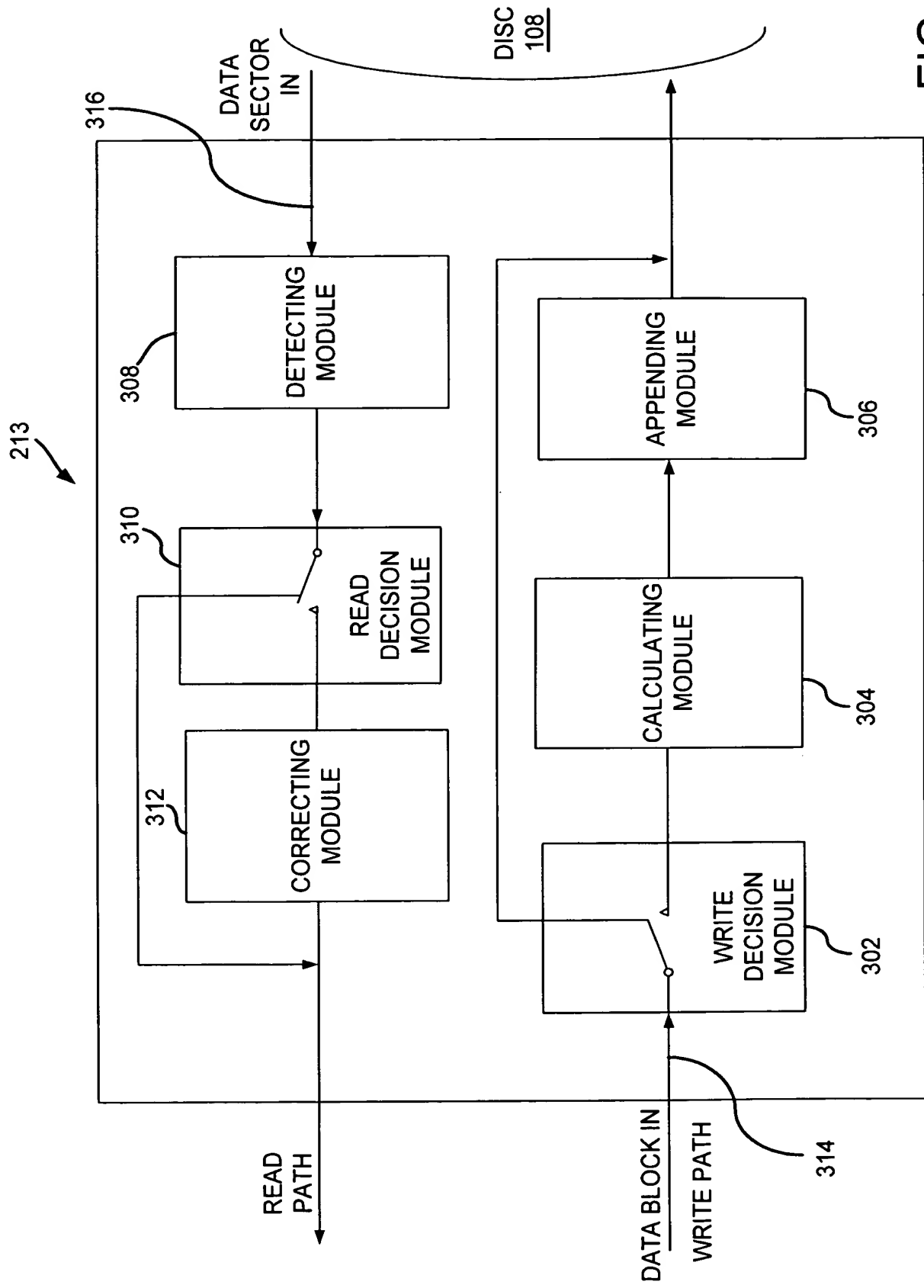


FIG. 3

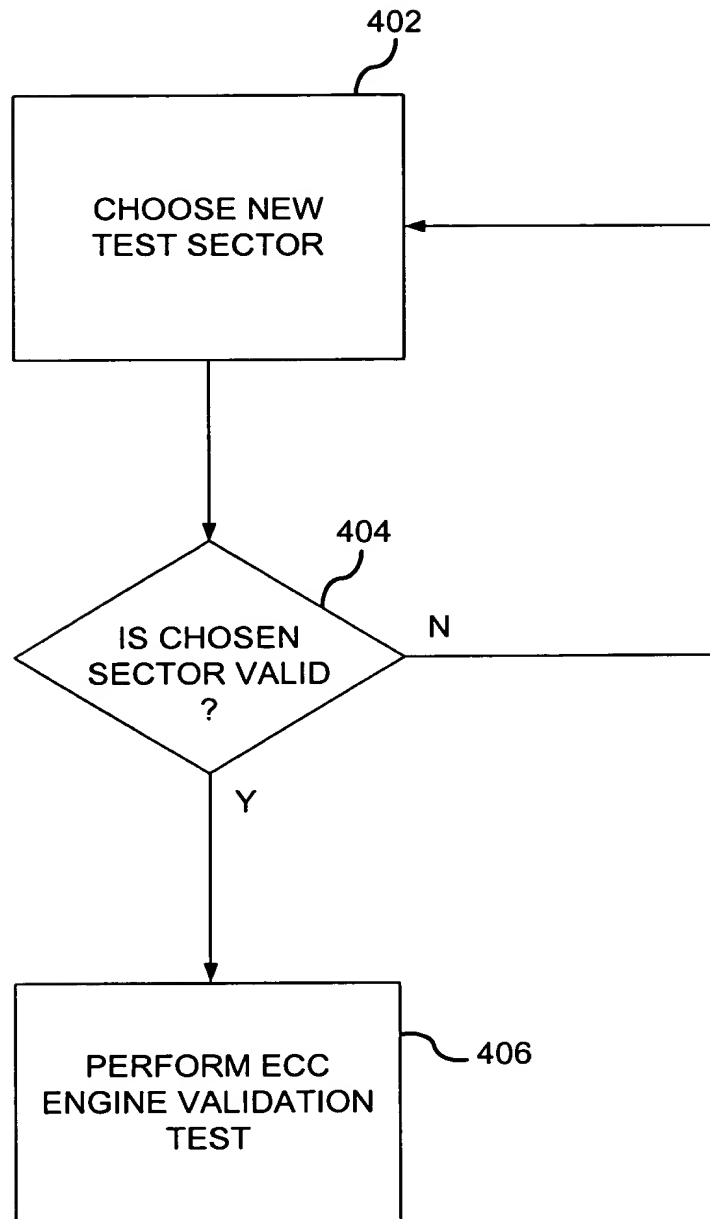


FIG. 4

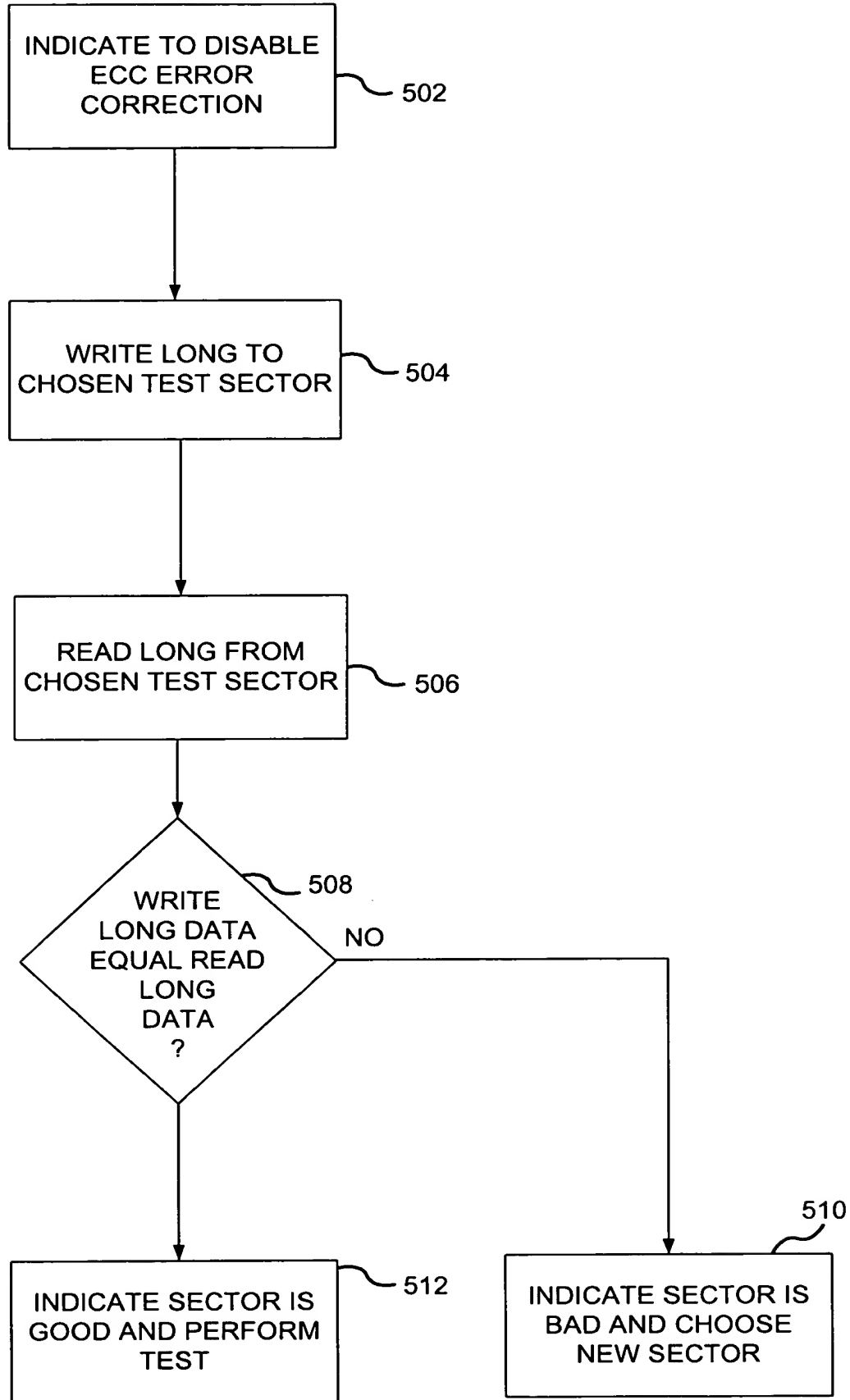


FIG. 5

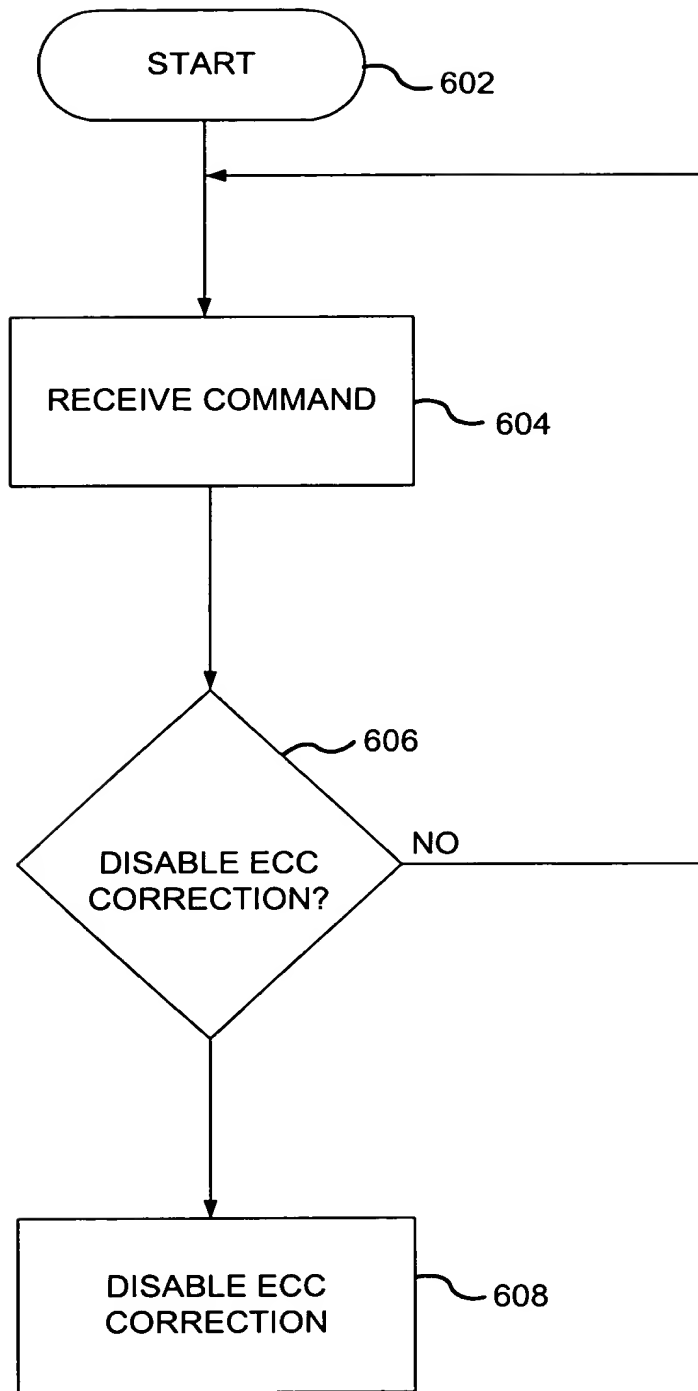


FIG. 6

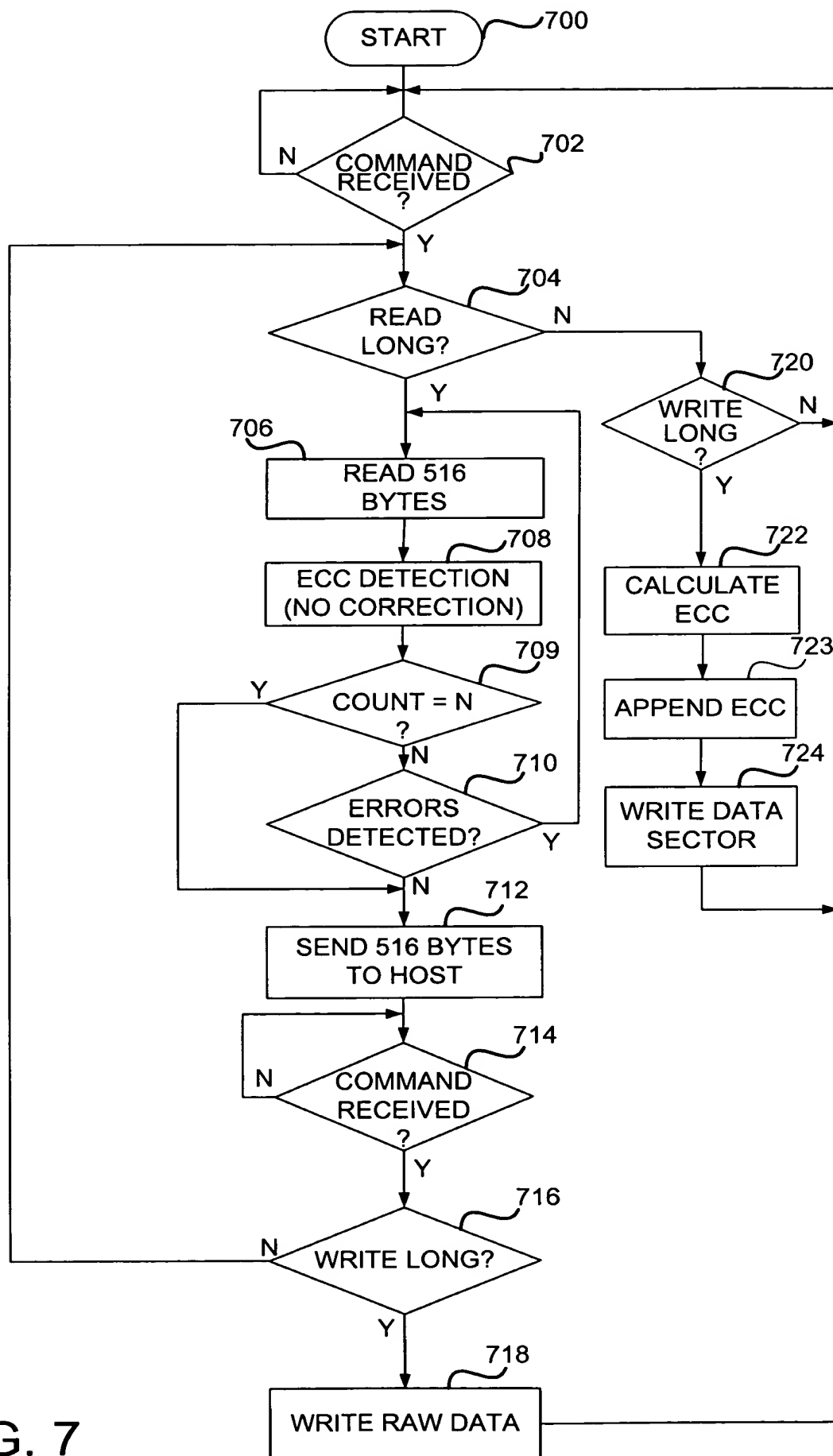


FIG. 7